


Faculty Profile

Name of the Teaching Staff	Dr. Vinaya Kumar S R	
Department	ECE	
E-mail	Vinayakumar.sr@cittumkur.org	
Present Designation	ASSOCIATE PROFESSOR	
Google Scholar ID	https://scholar.google.com/citations?hl=en&view_op=list_works&authuser=1&gmla=AKzYXQ1nx9vupaBXnazYxMJV-3Sv5eSinx3zawQ3itx28nlvq57_Zbv9mv3SLDt h1dLoTo_6YAI14AAATOfyrjGWe0P3&user=O_po5dMAAAAJ	
ORCID	0000-0001-8808-0532	
VIDWAN ID	201508	
Education Qualifications	<ol style="list-style-type: none"> UG-ECE PG-VLSI&EMBEDDED SYSTEM Ph.D-COMMUNICATION 	
Work Experience in Years		
Teaching	15	
Research	3	
Industry	---	
Other	NIL	
Total	15	
Area of Specialization	COMMUNICATION ,CMOS RF,RECTANTENNA VLSI& EMBEDDED SYSTEMS	
Publication Details	<p>Vinaya Kumar Sugganapalya Rajanna, Thimmegowda Venkatesh, Puneeth Kumar Tharehalli Rajanna, and Mudukavvanavar Shambulinga, "Independently Tunable Metamaterial Inspired Compact Triband DualSense Circularly Polarized Antenna" International Journal of Microwave and Wireless Technologies DOI https://doi.org/10.1017/S1759078724001247. (Scopus and SCI Indexed journal)</p> <p>Vinaya Kumar Sugganapalya Rajanna, Thimmegowda Venkatesh, Puneeth Kumar Tharehalli Rajanna, and Mudukavvanavar Shambulinga, "A Triband Slot Antenna Loaded with Asymmetric Split Ring Resonator for Wireless Applications," Progress In Electromagnetics Research Letters, Vol.117,61-67,2024. doi:10.2528/PIERL23110101 (Scopus indexed journal).</p>	

	<p>Vinaya Kumar Sugganapalya Rajanna “Wireless Assistive Communication Using Head Gestures and Voice Alerts for Paralyzed Patients” International Journal of Advanced Research in Computer and Communication Engineering Vol. 14, Issue 7, July 2025 DOI: 10.17148/IJARCCE.2025.14703</p> <p>Real-Time Flood Supervising and Forecasting using Simulated Neural Network” in International Journal of Innovative Science and Research Technology(IJISRT) p-1196 volume 3, Issue 5 March 2020,</p> <p>Design and Simulation of Compact Multiband Fractal Microstrip patch Antenna for C Band Applications” in International Journal of Innovative Research in Computer and Communication Engineering, p-ISSN: 2320-9801, Volume 4, Issue 6, June 2016.</p> <p>“Smart Automated Drip Irrigation System” in IJCESR, 2393 8374, Volume 4, Issue 9, Sep- 2017</p> <p>“Artificial Intelligence Based bank Cheque Signature verification system” in IJESC p-ISSN: 2250-1371, Volume 8, Issue 5, May 2018</p> <p>“Review on battery data online monitoring system for EV”, Electrical and Electronics Communication Robotics and Instrumentation engineering(ICEECIE-2016), 11th September, 2016, page Number1-5, ISBN Number-978- 93-85225-74-1.</p>
	<p>Vinaya Kumar S R, T.Venkatesh and P.K.TharehalliRajanna, "Characteristic Mode Analysis of Polarization Reconfigurable Compact Metasurface Antenna for sub-6GHz 5G Application, "2023 3rd International Conference on Mobile Networks and Wireless Communications (ICMNWC),Tumkur,India,2023, pp. 1-6,doi:10.1109/ICMNWC 60182.2023.1043870</p> <p>Vinaya Kumar SugganapalyaRajanna, ThimmegowdaVenkatesh, Puneeth KumarTharehalliRajanna, "Characteristic Mode Analysis of Wideband Patch Antenna Using Dual-Mode Resonance "IEEE global conference DOI: 10.1109/GCAT62922.2024.10923838</p> <p>“Online Battery Performance Enhancement and Optimization for EV”, International Conference on Recent Innovations in Engineering (ICrIE-2016), 8th and 9th October-2016, page Number-19.</p> <p>“Generation of multiband and design of microstrip fractal patch antenna and simulation for c band applications”, National Conference, NCRTEC-2017, P.NO 29, May 2017.</p>

	“Wireless sensor network controlled automated drip irrigation system”, National Conference, NCRTEC-2017, P.NO 16, May 2017.
Professional Memberships	MISTE.IETE
No. of Books published with details	---
Book Chapters published	---
Books Published	---
Reviewer in Journal/Conference	ICDTE-2025,ICSSES-2025
Patents	---
Academic & Administrative Responsibilities	<ul style="list-style-type: none"> ➤ Taught core subjects such as Analog Electronics, Microelectronics, Linear Integrated Circuits, Fundamentals of VLSI, Mechatronics, Basic Electronics, Basic Electrical Engineering, and Electronic Devices. ➤ Member of the Question Paper Scrutiny Committee and actively involved in monitoring Autonomous Examination processes. ➤ Served as NAAC Coordinator representing the Department of ECE. ➤ Part of the VTU Examination Processing Team, overseeing the examination workflow. ➤ Acted as Cultural Event Coordinator for multiple college fests, handling planning and execution. ➤ Served as Placement Coordinator, bridging student-industry interactions for the Department of ECE. ➤ Coordinated the Electronics Club, organizing workshops and technical activities. ➤ Functioned as ISTE Coordinator, facilitating professional development programs. ➤ Appointed to the Centre for Product Development (CPD) at SMVITM, Bantakal, Udupi. ➤ Served as Time Table Coordinator for effective academic scheduling in the ECE department. ➤ Actively participated in sports events at institutional level. ➤ Appointed as SPOC (Single Point of Contact) for ALS Pvt. Ltd., responsible for liaison with the industry and tracking project progress.

FDPs & Workshops Attended

- Participated in the Faculty Development Program on “Emerging Strategies in Teaching Pedagogy” organized by Channabasaveshwara Institute of Technology, in association with Brainovision Solutions India Pvt. Ltd. and in collaboration with AICTE, held from 23rd June to 27th June 2025.
- Participated in the Faculty Development Program on “Applications of Artificial Intelligence in Electrical Engineering” organized by the Department of Electrical & Electronics Engineering, Presidency School of Engineering, Presidency University, from 28th July to 1st August 2025.
- Participated in the five-day Faculty Development Program on “AI & IoT for Sustainable Future” organized by Ghousia College of Engineering, Ramanagara, from 28th July to 1st August 2025.
- Participated in the Faculty Development Program organized by Channabasaveshwara Institute of Technology in association with Brainovision Solutions India Pvt. Ltd., in collaboration with AICTE, held from 23rd to 27th June 2025.
- Participated in the Faculty Development Program on “Implementation of NSDC Internship Program” organized by Roman Technologies and VTU, from 2nd to 5th September 2024.
- Attended the National Seminar on “Inclusive Education Policy for Samarth Bharat” held at JRD Tata Auditorium, NIAS, Bengaluru, on 23rd August 2024.
- Participated in online teaching learning process using ICT tools for education by ST,Vincent Pallotti college of engineering and technology Nagpur from 8th Jun-13 June 2020.
- Participated in one Week Faculty Development Program On Machine learning using Python conducted by School of computing and information technology,Reva university banglore from 25 to 29th may 2020.
- Participated in the One Week Hands-on Faculty Development Program on “LabVIEW for Engineering Applications” organized by the Department of Electronics and Communication Engineering, Nagarjuna College of Engineering and Technology, Bengaluru, in association with NI LabVIEW Academy, held from 6th to 10th January 2020.
- Participated in the AICTE-sponsored One Week Short Term Training Programme (STTP) on “Advance Control Engineering and Simulation Using MATLAB and Simulink Tools” organized by the

Department of Electrical and Electronics Engineering, Ghousia College of Engineering, Ramanagaram, held from 26th to 31st August 2019.

- Participated in two-day work shop on “Analog and digital IC design using CADENCE tool” conducted by KIT Tiptur from 5th April to 6th April 2016.
- Participated in a Three-day work shop on “Recent trends in Vlsi and Embedded system design using Xilinx Zynq-7000 and mentor graphics EDA tools” conducted by SMVITM from 11th to 13th July 2016.
- Participated in a one-day workshop on “Fundamentals of analog integrated circuit design” conducted by Dr P. R. Mukund of Rochester institute of technology USA and organized by the Department of ECE SMVITM on 17 June 2015.
- AICTE sponsored Three-day FDP on “Advances in signal processing and its implementation
- using VLSI” from 22nd January 2014 to 24st January, 2014 held at PA College of Engineering, Mangalore.
- Six days Workshop on “Cipher system” from 20th to 25th July 2015 held at MIT, Manipal.
- Three days’ Workshop on “Writing Research Proposals, Intellectual Property Rights & Patenting” from 31st July to 2nd August 2017 held at ACS College of engineering Bangalore.
- Five days’ “RM and Enhancing Teaching Methodologies” from 11th Dec to 15th Dec 2017 held at SMVITM, Udupi.
- One-day Workshop on “Leadership Workshop” from 24th Jan 2017 held at SMVITM, Udupi.

FACULTY DEVELOPMENT /
TRAINING PROGRAMMES
COORDINATED

- Coordinated Faculty Development Program on “Emerging Strategies in Teaching Pedagogy” organized by Channabasaveshwara Institute of Technology, in association with Brainovision Solutions India Pvt. Ltd. and in collaboration with AICTE, held from 23rd June to 27th June 2025.
- Coordinated five-day Faculty Development Program on “AI & IoT for Sustainable Future” organized by Ghousia College of Engineering, Ramanagara, from 28th July to 1st August 2025.
- Coordinated One Week Hands-on Faculty Development Program on “LabVIEW for Engineering Applications” organized by the Department of Electronics and Communication Engineering, Nagarjuna College of Engineering and Technology, Bengaluru, in association with NI LabVIEW Academy, held from 6th to 10th January 2020.