


## Faculty Profile

Name of the Teaching Staff	SOWMYA M R	
Department	ECE	
E-mail	Sowmya.mr@cittumkur.org	
Present Designation	Assistant Professor	
Google Scholar ID	<a href="https://scholar.google.com/citations?view_op=list_works&amp;hl=en&amp;user=ry3CdnsAAAAJ">https://scholar.google.com/citations?view_op=list_works&amp;hl=en&amp;user=ry3CdnsAAAAJ</a>	
ORCID	<a href="https://orcid.org/0000-0002-2968-1239">https://orcid.org/0000-0002-2968-1239</a>	
VIDWAN ID	201520	
Education Qualifications	<ol style="list-style-type: none"> <li>UG-ECE</li> <li>PG-VLSI Design&amp; Embedded Systems</li> <li>Ph.D-</li> </ol>	
<b>Work Experience in Years</b>		
Teaching	4.8	
Research	-	
Industry	1	
Other	-	
Total	5.8	
Area of Specialization	VLSI Design& Embedded Systems	
Publication Details	<ol style="list-style-type: none"> <li>(Provide Complete Details) TILL DATE</li> </ol>	
Professional Memberships	IETE-AM-503886 IEI Membership No: AM1915005	
No. of Books published with details	NIL	
Book Chapters published	NIL	
Books Published	NIL	
Reviewer in Journal/Conference	*Presented a paper in 2 nd National Conference on “Emerging Trends in Electronics and Communication” at BGSIT. *Presented a paper in “e-Quest” 15 National Level Technical Conference at KIT. *Published a research paper in NCETEC-15 ISRASE Explore Digital Library at BGSIT.	
Patents	NIL	
Academic & Administrative Responsibilities	Result Analysis Coordinator For 2&3year Criteria-1 Coordinator Mom Coordinator	

FDPs & Workshops Attended	<p>Provide Complete Details with year and Title of Programme</p> <ol style="list-style-type: none"><li>1. AICTE – VTU Joint Teachers Training Programme “Analog VLSI Design using EDA Tools”- 20th – 25th March 2023</li><li>2. 5-day online FDP- “Inculcating Universal Human Values in Technical Education” from 13 September, 2021 to 17 September, 2021.</li></ol>

