


## Faculty Profile

Name of the Teaching Staff	BHAVYA B	
Department	ECE	
E-mail	<a href="mailto:bhavya.b@cittumkur.org">bhavya.b@cittumkur.org</a>	
Present Designation	ASSISTANT PROFESSOR	
Google Scholar ID		
ORCID		
VIDWAN ID	<b>201509</b>	
EducationQualifications	<ol style="list-style-type: none"> <li>BE in Electronics and Communication</li> <li>M.Tech in VLSI design and Embedded system</li> </ol>	
<b>WorkExperience in Years</b>		
Teaching	5 years 8 months	
Research		
Industry	1 year	
Other		
Total	6 years 8 months	
AreaofSpecialization	VLSI Design and Embedded system	
Publication Details	<p>"Dual Below-Threshold Supply Voltages for Energy Reduction in 45nm" National Conference on "Mixed Signal VLSI Design", Department of Electronics and Communication, Sri SatyaSai Institute of Science and Technology, Sehore, India, July- 2012.</p>	
ProfessionalMemberships	<p>IETE MISTE</p>	
No.ofBookspublishedwith details		
Book Chapters published		
Books Published		
Reviewer in Journal/Conference		
Patents		
Academic & Administrative Responsibilities	<p>Criteria 3 department coordinator for NAC, DepartmentTime table coordinator, DepartmentAlumni coordinator, Department Result analysis coordinator , Class Teacher for 4<sup>th</sup>sem B sec.</p>	
FDPs & Workshops Attended		

