


Faculty Profile

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| Name of the Teaching Staff | VINAYA KUMAR S R |  |
| Department | ECE | |
| E-mail | Vinayakumar.sr@cittumkur.org | |
| Present Designation | ASSISTANT PROFESSOR | |
| Google Scholar ID | --- | |
| ORCID | 0000-0001-8808-0532 | |
| VIDWAN ID | 201508 | |
| Education Qualifications | <ol style="list-style-type: none"> 1. UG-ECE 2. PG-VLSI&EMBEDDED SYSTEM 3. Ph.D-COMMUNICATION (PURSUING) | |
| Work Experience in Years | | |
| Teaching | 13 | |
| Research | --- | |
| Industry | --- | |
| Other | NIL | |
| Total | 13 | |
| Area of Specialization | VLSI& EMBEDDED SYSTEMS | |
| Publication Details | <p>Real-Time Flood Supervising and Forecasting using Simulated Neural Network” in International Journal of Innovative Science and Research Technology(IJISRT) p-1196 volume 3,Issue 5 March 2020,</p> <p>Design and Simulation of Compact Multiband Fractal Microstrip patch Antenna for C Band Applications” in International Journal of Innovative Research in Computer and Communication Engineering, p-ISSN: 2320-9801, Volume 4, Issue 6, June 2016.</p> <p>“Smart Automated Drip Irrigation System” in IJCESR, 2393 8374, Volume 4, Issue 9, Sep- 2017</p> <p>“Artificial Intelligence Based bank Cheque Signature verification system” in IJESC p-ISSN: 2250-1371, Volume 8, Issue 5, May 2018</p> <p>“Review on battery data online monitoring system for EV”, Electrical and Electronics Communication Robotics and Instrumentation engineering(ICEECIE-2016), 11th September, 2016, page Number1-5, ISBN Number-978- 93-85225- 74-1.</p> | |

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| | <p>"Online Battery Performance Enhancement and Optimization for EV", International Conference on Recent Innovations in Engineering (ICrIE-2016), 8th and 9th October-2016, page Number-19.</p> <p>"Generation of multiband and design of microstrip fractal patch antenna and simulation for c band applications", National Conference, NCRTEC-2017, P.NO 29, May 2017.</p> <p>"Wireless sensor network controlled automated drip irrigation system", National Conference, NCRTEC-2017, P.NO 16, May 2017.</p> |
| Professional Memberships | MISTE.IETE |
| No. of Books published with details | --- |
| Book Chapters published | --- |
| Books Published | --- |
| Reviewer in Journal/Conference | --- |
| Patents | --- |
| Academic & Administrative Responsibilities | <ul style="list-style-type: none"> ➤ Subject taught: Analog Electronics, Microelectronics, Linear integrated circuits, Fundamentals of VLSI, Mechatronics, Basic electronics, Basic electrical, Electronic devices. ➤ ISO coordinator representing Department of ECE. ➤ NAAC C-6 coordinator representing Department of ECE. ➤ NBA coordinator representing Department of ECE. ➤ Worked in exam processing team, Handled and monitored VTU examination process. ➤ Handled responsibilities of college fest, and acted as a cultural event coordinator for consecutive years. ➤ Time table coordinator representing Department of ECE. |
| FDPs & Workshops Attended | <ul style="list-style-type: none"> ➤ Participated in online teaching learning process using ict tools for education by ST,Vincent Pallotti college of engineering and technology Nagpur from 8th Jun-13 June 2020. ➤ Participated in one Week Faculty Development Program On Machine learning using Python conducted by School of |

computing and information technology,Reva university banglore from 25 to 29th may 2020.

- Participated in two-day work shop on "**Analog and digital IC design using CADENCE tool**" conducted by KIT Tiptur from 5th April to 6th April 2016.
- Participated in a Three-day work shop on "**Recent trends in Vlsi and Embedded system design using Xilinx Zynq-7000 and mentor graphics EDA tools**" conducted by SMVITM from 11th to 13th July 2016.
- Participated in a one-day workshop on "**Fundamentals of analog integrated circuit design**" conducted by Dr P. R. Mukund of Rochester institute of technology USA and organized by the Department of ECE SMVITM on 17 June 2015.
- AICTE sponsored Three-day FDP on "**Advances in signal processing and its implementation using VLSI**" from 22nd January 2014 to 24st January, 2014 held at PA College of Engineering, Mangalore.
- Six days Workshop on "**Cipher system**" from 20th to 25th July 2015 held at MIT, Manipal.
- Three days' Workshop on "**Writing Research Proposals, Intellectual Property Rights & Patenting**" from 31st July to 2nd August 2017 held at ACS college of engineering Bangalore.
- Five days' "**RM and Enhancing Teaching Methodologies**" from 11th Dec to 15th Dec 2017 held at SMVITM, Udupi.
- One-day Workshop on "**Leadership Workshop**" from 24th Jan 2017 held at SMVITM, Udupi.