


## Faculty Profile

Name of the Teaching Staff	ROOPA S	
Department	Electronics and Communication Engineering	
E-mail	roopa.s@cittumkur.org	
Present Designation	Assistant Professor	
Google Scholar ID		
ORCID		
VIDWAN ID		
Education Qualifications	<ol style="list-style-type: none"> <li>1. UG: <b>Electronics and Communication Engineering from VTU</b></li> <li>2. PG: Signal Processing from SIT, Tumkur</li> <li>3. Ph.D</li> </ol>	
<b>Work Experience in Years</b>		
Teaching	11 Years	
Research	--	
Industry	--	
Other	--	
Total	11 Years	
Area of Specialization	Signal Processing	
Publication Details	<p>"Fully Pipelined Baseline JPEG Encoder For Real Time Applications Using FPGA", Proceedings of National Conference on Convergence of signal processing, communication and VLSI Design, SDM College of Engineering, Dharwad, 13<sup>th</sup> and 14<sup>th</sup> August 2010.</p>	
Professional Memberships		
No. of Books published with details		
Book Chapters published		
Books Published		
Reviewer in Journal/Conference		
Patents		
Academic & Administrative Responsibilities		
FDPs & Workshops Attended	FDP on "Control Systems & Sensors Technology" from 2020-10-5 to 2020-10-9 at University visweswaraya college of Engineering.	

