

FIFTH SEMESTER

Common to EC/TE

Subject Code	Title	Teaching Department	Teaching hours/week		Examination			
			Theory	Practical	Duration	I. A	Theory/ Practical	Total Marks
06AL - 51	Management & Entrepreneurship	EC	04		03	25	100	125
06EC- 52	Digital signal Processing	EC	04		03	25	100	125
06EC 53	Analog Communication	EC	04		03	25	100	125
06EC - 54	Microwaves and Radar	EC	04		03	25	100	125
06EC-55	Digital Switching Systems	EC	04		03	25	100	125
06EC - 56	Fundamentals of CMOS VLSI	EC	04		03	25	100	125
06ECL - 57	DSP Lab	EC		03	03	25	50	75
06ECL - 58	Analog Communication Lab + LIC Lab	EC		03	03	25	50	75
		<b>Total</b>	<b>24</b>	<b>06</b>	<b>24</b>	<b>200</b>	<b>700</b>	<b>900</b>

SIXTH SEMESTER

for EC Branch

Subject Code	Title	Teaching Department	Teaching hours/week		Examination			
			Theory	Practical	Duration	I. A	Theory/ Practical	Total Marks
06EC - 61	Digital Communication	EC	04		03	25	100	125
06EC - 62	Microprocessors	EC	04		03	25	100	125
06EC - 63	<a href="#">Analog and Mixed mode VLSI Design</a>	EC	04		03	25	100	125
06EC - 64	<a href="#">Antennas and Propagation</a>	EC	04		03	25	100	125
06EC - 65	Information theory and coding	EC	04		03	25	100	125
06EC - 66X	Elective -(Group -A)	EC	04		03	25	100	125
06ECL - 67	<a href="#">Advanced Communication Lab</a>	EC		03	03	25	50	75
06ECL - 68	Microprocessors Lab	EC		03	03	25	50	75
		<b>Total</b>	<b>24</b>	<b>06</b>	<b>24</b>	<b>200</b>	<b>700</b>	<b>900</b>

Electives -1(Group A)

06EC661	Programming in C ++	06EC664	Adaptive Signal Processing
06EC662	Satellite Communications	06EC665	Low Power VLSI Design
06EC663	Random Processes	06EC666	Modern Control theory
		06EC667	Digital System Design Using VHDL

## SEVENTH SEMESTER

## for EC Branch

Subject Code	Title	Teaching Department	Teaching hours/week		Examination			
			Theory	Practical	Duration	I. A	Theory/ Practical	Total Marks
06EC-71	Computer communication Network	EC	04		03	25	100	125
06EC – 72	Optical Fiber Communication	EC	04		03	25	100	125
06EC – 73	Power Electronics	EC	04		03	25	100	125
06EC - 74	DSP Algorithms & Architecture	EC	04		03	25	100	125
06TC -75X	Elective -2 (Group B)		04		03	25	100	125
06TC – 76X	Elective -3 (Group C)		04		03	25	100	125
06TCL – 77	VLSI Lab			03	03	25	50	75
06TCL - 78	Power Electronics Lab			03	03	25	50	75
		Total	24	06	24	200	700	900

## Electives -2(Group B)

06EC751	Operating Systems	06EC755	ATM Networks
06EC752	Pattern Recognition	06EC756	Image Processing
06EC753	Artificial Neural Network	06EC757	Video Engineering
06EC754	CAD For VLSI	06MS769	Micro & Smart Sv. Technology

## Electives -3(Group C)

06EC761	Data Structures using C++	06EC764	Wavelet Transforms
06TC76 2	Real Time Systems	06EC765	Modeling & Simulation of Data Networks
		06EC766	Speech Processing
06EC763	Radio Frequency Integrated Circuits	06EC767	H R Management

## EIGHTH SEMESTER

## for EC Branch

Subject Code	Title	Teaching Department	Teaching hours/week		Examination			
			Theory	Practical	Duration	I. A	Theory/ Practical	Total Marks
06EC-81	Wireless Communication	EC	04		03	25	100	125
06EC – 82	Embedded System Design	EC	04		03	25	100	125
06EC - 83X	Elective -4 (Group D)	EC	04		03	25	100	125
06EC – 84X	Elective - 5 (Group E)	EC	04		03	25	100	125
06EC – 85	Project Work	EC	-	06	03	50	100	150
06EC - 86	Seminar	EC	-	-	-	50	-	50
		Total	16	06	24	200	500	700

## Electives -4(Group D)

06EC831	Distributed Systems	06EC834	Biomedical Signal Processing
06EC832	Network Security	06EC835	High performance computer networks
06EC833	Internet Engineering	06EC836	Fuzzy Logic

## Electives -5(Group E)

06EC841	Multimedia Communication	06EC844	GSM
06TC7842	Real Time Operating Systems	06EC845	Adhoc Wireless Networks
06EC843	Optical Networks	06EC846	Optical Computing